

LIC (MOCK)

(1MKS)

1.	Specified value of CMRR for 741 opamp is _____. 70dB 80dB 90dB 100dB
2.	The function of a differential amplifier is to amplify input signals to amplify the difference between two input signals to find difference between two input signals to amplify and differentiate two input signals
3.	Average of two input current of op-amp is _____. Input offset Current Input Bias Current Input offset Voltage Output Offset Current
4.	Select correct voltage gain formula for Non-inverting Amplifier $-(R_f/R)$ $1+(R_f/R)$ (R_f/R) 1
5.	If step input is given to Integrator circuit output will be Square wave Ramp Cosine Impulse
6.	Differentiator circuit is what type of filter Low pass filter High pass filter Band pass filter Band reject filter
7.	RC Phase shift oscillator 's gain must be 3 29 6 1
8.	Which ADC is known as fastest ADC Flash type ADC Successive approximation ADC Single slope ADC Dual slope ADC
9.	What type of wave form generated by square wave generator, at across capacitor

	square wave sawtooth Cosine Triangular wave
10.	Which voltage regulator IC is used for positive adjustable voltage 7805 317 337 7912
11.	In IC-555 pin no. 7 is known as +Vcc Threshold Discharge Ground
12.	Astable multivibrator does not require _____ input external trigger internal trigger Vcc ground
13.	The frequency of the VCO can be changed by changing external component ____ inductor capacitor transistor transformer
14.	What will be the output of a IC 7812? +12 -12 78 3
15.	The output voltage of phase detector use in PLL is Phase voltage Discrete voltage Error voltage Always 0
16.	What is the voltage gain of the Voltage follower circuit? 0 1 -1 infinity

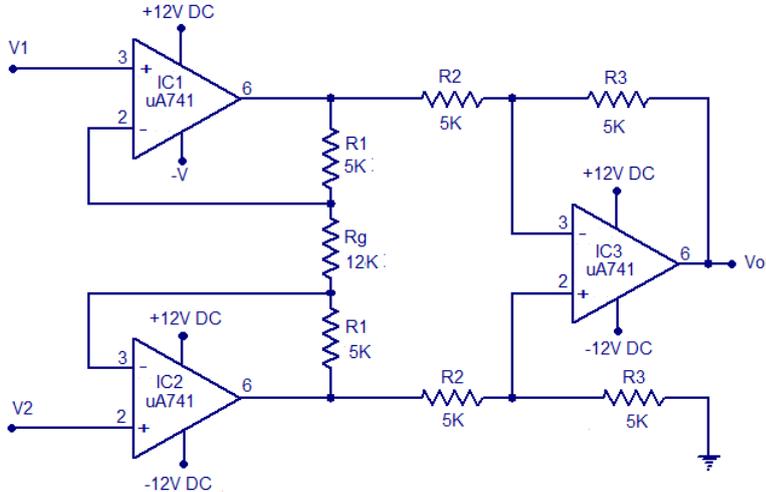
(2MKS)

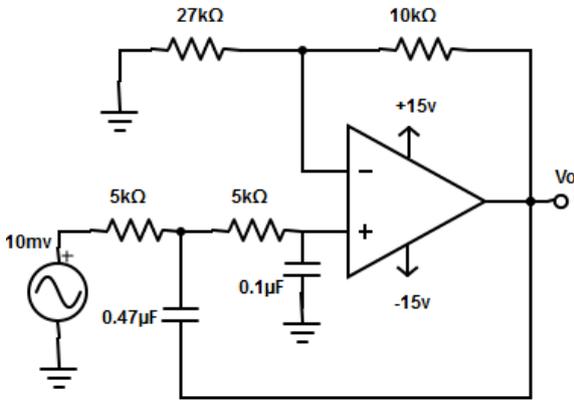
17.	Calculate the voltage regulation of a power supply having $V_{NL} = 12\text{ V}$ and $V_{FL} = 12\text{ V}$ 0%
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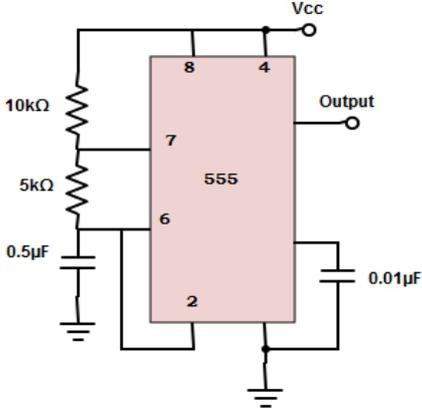
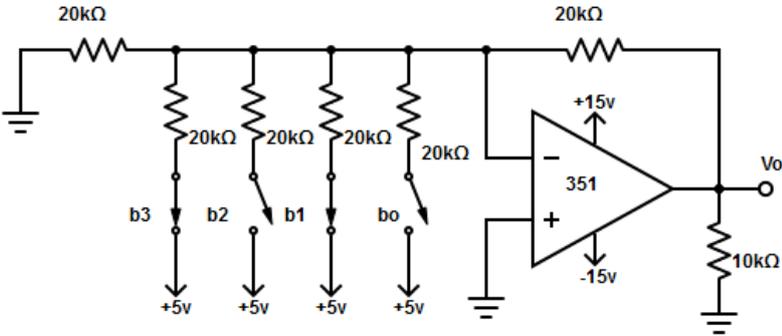
	<p>10%</p> <p>20%</p> <p>30%</p>
18.	<p>Wein bridge having $R = 100\text{kohms}$, $C = 1\text{nF}$. calculate frequency of oscillation.</p> <p>1.6kHz</p> <p>16kHz</p> <p>650Hz</p> <p>6.5kHz</p>
19.	<p>A Second Order Low Pass Filter having $R1=R2 = 10\text{k}\Omega$ and $C1=C2 = 0.1\mu\text{F}$. Calculate cut-off frequency</p> <p>1.5kHz</p> <p>159Hz</p> <p>100Hz</p> <p>2kHz</p>
20.	<p>Which are the correct statements for an integrator circuit -</p> <p>Statement 1- It is inverting amplifier</p> <p>Statement 2- It is non-inverting amplifier</p> <p>Statement 3- Uses positive feedback</p> <p>Statement 4- Uses negative feedback</p> <p>Statements 1& 3 are correct.</p> <p>Statements 2& 4 are correct.</p> <p>Statements 1& 4 are correct.</p> <p>Statements 2& 3 are correct.</p>
21.	<p>Series pass transistor always operates in the_____region in a linear IC voltage regulator</p> <p>Active</p> <p>Saturation</p> <p>Cut-off</p> <p>transient</p>
22.	<p>What is the value of current I_{ADJ} in Voltage regulator IC -LM317?</p> <p>10 micro Amperes</p> <p>50 micro Amperes</p> <p>100 micro Amperes</p> <p>150 micro Amperes</p>
23.	<p>Voltage 0.3 V can be rectified by</p> <p>Half wave rectifier</p> <p>Full wave rectifier</p> <p>precision rectifier</p> <p>bridge rectifier</p>
24.	<p>The range of frequencies over which the PLL can acquire lock with an input signal is called as capture range</p> <p>True</p> <p>False</p>

25.	<p>In IC-555 Voltage at pin no. 5 is</p> <ul style="list-style-type: none"> 1/3 Vcc 2/3 Vcc 1/2 Vcc Vcc
26.	<p>Change in output voltage for a change in input voltage is known as</p> <ul style="list-style-type: none"> Load regulation Line regulation voltage regulation current regulation

(2MKS)

27.	<p>Calculate gain for given circuit</p>  <p>18 5 1.8 0.5</p>
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28.	<p>Calculate the high cut-off frequency for the circuit given</p>  <p>589Hz 185Hz</p>
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	<p>147Hz 104Hz</p>
<p>29.</p>	<p>Find T_{on} time for given circuit.</p>  <p>5ms 3ms 2ms 1ms</p>
<p>30.</p>	<p>Calculate the output voltage for LM317 regulator, if $R_1=240\Omega$ $R_2=5k\Omega$.</p> <p>27v 32v 34v 22v</p>
<p>31.</p>	<p>To design Monostable M/V with delay time 11ms and $C=0.1\mu F$. What will be the value of $R=?$</p> <p>100Ω 1kΩ 100kΩ 1MΩ</p>
<p>32.</p>	<p>For the given circuit find the output voltage?</p>  <p>-4.6V -3.13V -3.83V -5V</p>

33.	An emitter bias Dual Input Balanced Output differential amplifier has $V_{CC}=12\text{v}$ and $R_E=2\text{k}\Omega$. Find I_E 7.42mA 2.8mA 10mA 8.6mA